

Refinement trees: Calculi, Tools and Applications

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Start with a **requirement specification** SP_0 which only fixes the expected properties of the software system.

At each **refinement step**, add more details of the design, until the specification reached can be easily implemented by a program.

$$SP_0 \rightsquigarrow \left\{ \begin{array}{l} SP_1 \rightsquigarrow P_1 \\ \vdots \\ SP_n \rightsquigarrow \left\{ \begin{array}{l} SP_{n1} \rightsquigarrow \{ SP_{n11} \rightsquigarrow P_{n11} \\ \dots \\ SP_{nm} \rightsquigarrow P_{nm} \end{array} \right. \end{array} \right.$$

- ▶ explicit representation of CASL refinement language as **refinement trees**
- ▶ prove that refinements are **correct**
- ▶ prove that refinements are **consistent**
- ▶ applications: consistency of large theories - DOLCE
- ▶ implement all of the above in Hets



This work extends:

- ▶ T. Mossakowski, D. Sannella, A. Tarlecki - “A Simple Refinement Language for CASL”, WADT 2004 [CASL-Ref]
- ▶ P. Hoffman - “Architectural Specification Calculus”, Chapter IV.5 of CASL Reference Manual [CASL-RM]

CASL, the Common Algebraic Specification Language



specification libraries

architectural refinements

structured specifications

sorted first-order logic
+ partiality + induction

CASL, the Common Algebraic Specification Language



specification libraries

architectural refinements

structured specifications

put your favorite
logical system here!

Institutions formalize logical systems (Goguen/Burstall 1984)

An **institution** consists of:

- ▶ a category **Sign** of **signatures**;
- ▶ a functor **Sen**: **Sign** \rightarrow **Set**, giving a set **Sen**(Σ) of **Σ -sentences** for each signature $\Sigma \in |\mathbf{Sign}|$. Notation: **Sen**(σ)(φ) is written **$\sigma(\varphi)$** ;
- ▶ a functor **Mod**: **Sign**^{op} \rightarrow **Cat**, giving a category **Mod**(Σ) of **Σ -models** for each $\Sigma \in |\mathbf{Sign}|$. Notation: **Mod**(σ)(M') is written **$M'|_{\sigma}$** ;
- ▶ for each $\Sigma \in |\mathbf{Sign}|$, a **satisfaction relation** $\models_{\Sigma} \subseteq |\mathbf{Mod}(\Sigma)| \times \mathbf{Sen}(\Sigma)$ such that for any $\sigma: \Sigma \rightarrow \Sigma'$, $\varphi \in \mathbf{Sen}(\Sigma)$ and $M' \in \mathbf{Mod}(\Sigma')$:

$$M' \models_{\Sigma'} \sigma(\varphi) \iff M'|_{\sigma} \models_{\Sigma} \varphi \quad [\textit{Satisfaction condition}]$$

Specification frames formalize the notion of logical theory (Ehrig/Pepper/Orejas 1989).

A **specification frame** is an indexed category $\mathbf{Mod}: \mathbf{Th}^{op} \rightarrow \mathbf{Cat}$.

$$\begin{array}{ccc} T_1 & & \mathbf{Mod}(T_1) \\ \downarrow \sigma & & \uparrow \mathbf{Mod}(\sigma) = _|\sigma \\ T_2 & & \mathbf{Mod}(T_2) \end{array}$$

We assume that \mathbf{Th} is (finitely) cocomplete.

Moreover, we assume that \mathbf{Th} comes with an inclusion system (\Rightarrow unions).

Sometimes we also need that \mathbf{Mod} takes colimits to limits (amalgamation property).

In this work, we work over an **arbitrary specification frame**.
Examples use first-order logic, with CASL notation.

$$SP ::= Th \mid SP_1 \text{ and } SP_2 \mid SP \text{ with } \sigma \mid SP \text{ hide } \sigma$$

Mod(*Th*) is given above

Mod(*SP*₁ and *SP*₂) = **Mod**(*t*₁)⁻¹(**Mod**(*SP*₁)) ∩ **Mod**(*t*₂)⁻¹(**Mod**(*SP*₂))

Mod(*SP* with σ) = **Mod**(σ)⁻¹(**Mod**(*SP*))

Mod(*SP* hide σ) = **Mod**(σ)(**Mod**(*SP*))

Branching points are represented in CASL as **architectural specifications**.

arch spec ADDITION_FIRST =

units

$N : \text{NAT};$

$F : \text{NAT} \rightarrow \{\mathbf{op} \quad \text{*suc*}(n : \text{Nat}) : \text{Nat} = n + 1\};$

result $F [N]$

An architectural specification is correct if the models of its units can be combined as prescribed by the result unit expression.

$$\begin{aligned}
 ASP & ::= S \mid \mathbf{units} \ UDD_1 \dots UDD_n \ \mathbf{result} \ UE \\
 UDD & ::= UDEFN \mid UDECL \\
 UDECL & ::= UN : USP \langle \mathbf{given} \ UT_1, \dots, UT_n \rangle \\
 USP & ::= SP \mid SP_1 \times \dots \times SP_n \rightarrow SP \mid ASP \\
 UDEFN & ::= UN = UE \\
 UE & ::= UT \mid \lambda \ A_1 : SP_1, \dots, A_n : SP_n \bullet UT \\
 UT & ::= UN \mid F \ [FIT_1] \dots [FIT_n] \mid UT \ \mathbf{and} \ UT \mid UT \ \mathbf{with} \ \sigma : \Sigma \rightarrow \Sigma' \mid \\
 & \quad UT \ \mathbf{hide} \ \sigma : \Sigma \rightarrow \Sigma' \mid \mathbf{local} \ UDEFN_1 \dots UDEFN_n \ \mathbf{within} \ UT \\
 FIT & ::= UT \mid UT \ \mathbf{fit} \ \sigma : \Sigma \rightarrow \Sigma'
 \end{aligned}$$

Checks whether an architectural specification ASP has a denotation and that the units produced by ASP satisfy a given unit specification USP - denoted $\vdash ASP :: USP$.

- ▶ based on a **diagram** D_{UT} for unit terms UT (of dependencies between units), where nodes are labeled with sets of specifications.
- ▶ verification conditions are discharged in a quite complicated manner.
- ▶ in [CASL-RM], the architectural language is restricted.
- ▶ unit imports left out due to increased complexity.

- ▶ extract the specification of each unit expression and uses it to compute the specification of the result unit - denoted $\vdash ASP ::_c USP$.

Let ASP be an architectural specification and UT a unit term. Then the specification of UT , denoted $\mathcal{S}_{ASP}(UT)$ is defined as follows:

- ▶ if UT is a unit name, then $\mathcal{S}_{ASP}(UT) = SP$ where $UT : SP$ is the declaration of UT in ASP ;
- ▶ if $\mathcal{S}_{ASP}(A_i) = SP_i$ then $\mathcal{S}_{ASP}(A_1 \text{ and } \dots \text{ and } A_n) = SP_1 \text{ and } \dots \text{ and } SP_n$;
- ▶ if $\mathcal{S}_{ASP}(A) = SP$, then $\mathcal{S}_{ASP}(A \text{ with } \sigma) = SP \text{ with } \sigma$;
- ▶ if $\mathcal{S}_{ASP}(A) = SP$, then $\mathcal{S}_{ASP}(A \text{ hide } \sigma) = SP \text{ hide } \sigma$;
- ▶ if $UT = F[UT_1 \text{ fit } \sigma_1] \dots [UT_n \text{ fit } \sigma_n]$, where $\mathcal{S}_{ASP}(F) = SP_1 \times \dots \times SP_n \rightarrow SP$ and for any $i = 1, \dots, n$, $\mathcal{S}_{ASP}(UT_i) \rightsquigarrow SP_i \text{ with } \sigma_i$, then $\mathcal{S}_{ASP}(UT) = \{SP \text{ with } \sigma\}$ and $\mathcal{S}_{ASP}(UT_1) \text{ with } l_1; l'$ and \dots and $\mathcal{S}_{ASP}(UT_n) \text{ with } l_n; l'$;

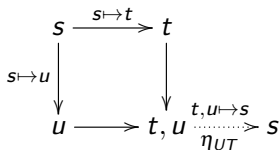
The specification of unit terms is sound, but too weak (i.e. incomplete).

arch spec ASP =

units $U : \text{sort } s;$

$UT = (U \text{ with } s \mapsto t) \text{ and } (U \text{ with } s \mapsto u)$

result UT



Let ASP be an architectural specification and UT a unit term. Then the specification of UT , denoted $\mathcal{S}_{ASP}(UT)$ is defined as follows:

- ▶ if UT is a unit name, then $\mathcal{S}_{ASP}(UT) = SP$ where $UT : SP$ is the declaration of UT in ASP ;
- ▶ if $UT = F[UT_1 \text{ fit } \sigma_1] \dots [UT_n \text{ fit } \sigma_n]$, where $\mathcal{S}_{ASP}(F) = SP_1 \times \dots \times SP_n \rightarrow SP$ and for any $i = 1, \dots, n$, $\mathcal{S}_{ASP}(UT_i) \models SP_i$ **with** σ_i , then $\mathcal{S}_{ASP}(UT) = \{SP \text{ with } \sigma\}$ **and** $\mathcal{S}_{ASP}(UT_1)$ **with** $\iota_1; \iota'$ **and** \dots **and** $\mathcal{S}_{ASP}(UT_n)$ **with** $\iota_n; \iota'$ **and** $\mathbf{S}_{colim}(UT)$;
- ▶ if $UT = A_1$ **and** \dots **and** A_n and $\mathcal{S}_{ASP}(A_i) = SP_i$ then $\mathcal{S}_{ASP}(UT) = SP_1$ **and** \dots **and** SP_n **and** $\mathbf{S}_{colim}(UT)$;
- ▶ (rest remains)

where $\mathbf{S}_{colim}(UT) = Colim(D_{UT})$ **hide** η_{UT} ,
 $\eta_{UT} : Sig(UT) \rightarrow Colim(D_{UT})$ is the colimit injection of UT and D_{UT}
is the diagram of UT .

Theorem

If there are no imports and no generic unit is applied more than once,
Mod($\mathcal{S}_{ASP}(UE)$) = ProjRes(**Mod**(ASP)), where UE is the result unit expression of ASP.

Conjecture

*With a **generative** semantics for architectural specifications,*
Mod($\mathcal{S}_{ASP}(UE)$) = ProjRes(**Mod**(ASP)).

$$\frac{\begin{array}{c} \Gamma_{\emptyset} \vdash UDD_1 ::_c \Gamma_1 \\ \vdots \\ \Gamma_{n-1} \vdash UDD_n ::_c \Gamma_n \end{array}}{\vdash \mathbf{units} \ UDD_1 \dots UDD_n \ \mathbf{result} \ UE ::_c \mathcal{S}_{\Gamma_n}(UE)}$$

$$\frac{\vdash UDECL ::_c \Gamma'}{\Gamma \vdash UDECL \ \mathbf{qua} \ UDD ::_c \Gamma \cup \Gamma'}$$

$$\frac{\Gamma \vdash UDEFN ::_c \Gamma'}{\Gamma \vdash UDEFN \ \mathbf{qua} \ UDD ::_c \Gamma'}$$

$$\frac{\vdash SPR ::_c (USP, BSP)}{\vdash UN : SPR ::_c \{UN \mapsto USP\}}$$

$$\frac{}{\Gamma \vdash UN = UE ::_c \Gamma \cup \{UN \mapsto \mathcal{S}_{\Gamma}(UE)\}}$$

Assume: ASP has no unit imports, is syntactically correct, and each parametric unit is consistent and applied only once.

Theorem

$\vdash ASP ::_c USP$ implies $\vdash ASP :: USP$.

Theorem

If $\vdash ASP :: USP$ for some USP , then $\vdash ASP ::_c USP'$ where USP' is the specification of the result unit of ASP and moreover $USP' \rightsquigarrow USP$.

Corollary

$\vdash ASP ::_c USP$ implies $\text{ProjRes}(\mathbf{Mod}(ASP)) \subseteq \mathbf{Mod}(USP)$.

Corollary

If $\text{ProjRes}(\mathbf{Mod}(ASP)) \subseteq \mathbf{Mod}(USP)$ then $\vdash ASP ::_c USP'$ and $USP' \rightsquigarrow USP$.

The simplest form: **model class inclusion**. [CASL-Ref] introduces the following syntax:

refinement $R1 = \text{MONOID}$ **refined via** $Elem \mapsto Nat$ **to** NAT

Correctness of this refinement means that

$$M|_{\sigma} \in \llbracket \text{MONOID} \rrbracket \text{ for each } M \in \llbracket \text{NAT} \rrbracket$$

where σ maps $Elem$ to Nat .

Refinements can be composed in chains of refinements:

refinement $R1 = \text{MONOID}$ **refined via** $Elem \mapsto Nat$ **to** NAT

refinement $R2 = \text{NAT}$ **refined via** $Nat \mapsto Bin$ **to** NATBIN

refinement $R3 = R1$ **then** $R2$

Composition is defined only if the corresponding signatures match in the sense of [CASL-Ref].

Architectural specifications express **branching points** in refinements.

```
arch spec ADDITION_FIRST =  
  units
```

```
    N : NAT;
```

```
    F : NAT → {op   suc(n : Nat) : Nat = n + 1};
```

```
result F [N]
```

```
refinement R4 =
```

```
  NATWITHSUC refined to arch spec ADDITION_FIRST
```

An architectural specification is correct if the models of its units can be combined as prescribed by the result unit expression.

Unit imports (written **given** N) are shorthand for parametric units that are applied once.

```
arch spec ADDITION_FIRST =
```

```
units
```

```
 $N : \text{NAT};$ 
```

```
 $M : \text{NATWITHSUC}$  given  $N;$ 
```

```
result  $M$ 
```

```
    arch spec ADDITION_FIRST =
```

```
    units
```

```
     $N : \text{NAT};$ 
```

```
means  $M : \mathbf{arch\ spec\ \{units}$ 
```

```
     $F : \text{NAT} \rightarrow \text{NATWITHSUC};$ 
```

```
    result  $F [N]\}$ 
```

```
result  $M$ 
```

```
arch spec ADDITION_FIRST =  
  units  
    N : NAT;  
    M : NATWITHSUC given N;  
  result M
```

Components of architectural specifications can be further refined:

```
refinement R =  
  arch spec ADDITION_FIRST then {N to R2}
```

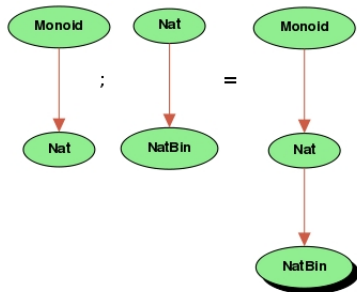
It is possible to refine more than one component at once (for example M could also be refined).

- ▶ nodes are labeled with unit specifications
- ▶ two types of links: refinement links and component links
- ▶ "grow" both at the root and at the leaves
- ▶ come with an auxiliary structure for managing **compositions**

refinement R1 =
MONOID **refined via** $Elem \mapsto Nat$ **to** NAT

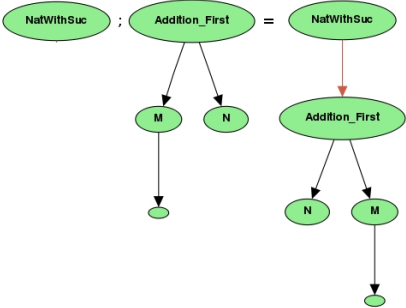
refinement R2 =
NAT **refined via** $Nat \mapsto Bin$ **to** NATBIN

refinement R3 =
R1 **then** R2



arch spec ADDITION_FIRST =
units
N : NAT;
M : NATWITHSUC **given** N;
result M

refinement R4 =
NATWITHSUC **then**
arch spec ADDITION_FIRST



arch spec ADDITION_FIRST =

units

$N : \text{NAT};$

$M : \text{NATWITHSUC}$ **given** $N;$

result M

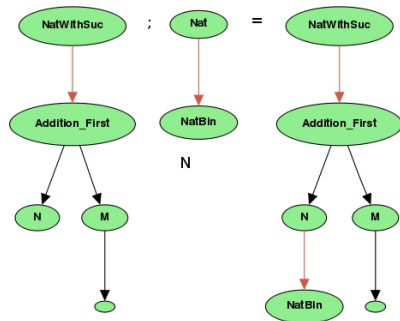
refinement $R2 =$

NAT **refined via** $\text{Nat} \mapsto \text{Bin}$ **to** NATBIN

refinement $R =$

arch spec ADDITION_FIRST **then**

$\{N \text{ to } R2\}$



Checks whether a refinement specification has a denotation and also constructs its refinement tree.

- ▶ based on $\vdash ASP ::_c USP$ for architectural specifications.
- ▶ extends to the refinement language in a natural way - specifications of units are now arbitrary refinements.
- ▶ unit imports can be replaced by an equivalent construction using the specification of the imported unit and raise no increase in complexity.

$$\frac{(n, \mathcal{RT}) = \mathcal{RT}_0[USP]}{\vdash USP ::_c (USP, USP), \mathcal{RT}, (n, n)}$$

$$\begin{aligned} &\vdash USP ::_c (USP, USP), \mathcal{RT}_1, p_1 \\ &\vdash SPR ::_c (USP', BSP), \mathcal{RT}_2, p_2 \\ &(\mathcal{RT}, p) = \mathcal{RT}_1 \circ_{p_1, p_2} \mathcal{RT}_2 \\ &USP \rightsquigarrow_{\sigma} USP' \end{aligned}$$

$$\vdash USP \text{ refined via } \sigma \text{ to } SPR ::_c (USP' \text{ hide } \sigma, BSP), \mathcal{RT}, p$$

$$\frac{\begin{array}{l} \vdash ASP ::_c USP \\ \vdash SPR_i ::_c (USP_i, BSP_i), \mathcal{RT}_i, p_i \\ \text{for any } UN_i : SPR_i \text{ in } ASP \\ SPM(UN_i) = BSP_i \\ (n, \mathcal{RT}') = \mathcal{RT}_\emptyset[USP] \\ \mathcal{RT} = \mathcal{RT}'[n \rightarrow \mathcal{RT}_1, \dots, \mathcal{RT}_k] \\ p = (n, \{UN_i \mapsto p_i\}_{i=1, \dots, k}) \end{array}}{\vdash ASP ::_c (USP, SPM), \mathcal{RT}, p}$$

$$\frac{\begin{array}{l} \vdash SPR_i ::_c S_i, \mathcal{RT}_i, p_i \\ \mathcal{RT} = \cup \mathcal{RT}_i \\ p = \{UN_i \rightarrow p_i\} \end{array}}{\vdash \{UN_i \text{ to } SPR_i\}_{i \in \mathcal{I}} ::_c \{UN_i \rightarrow S_i\}_{i \in \mathcal{I}}, \mathcal{RT}, p}$$

$$\frac{\begin{array}{l} \vdash SPR_1 ::_c S_1, \mathcal{RT}_1, p_1 \\ \vdash SPR_2 ::_c S_2, \mathcal{RT}_2, p_2 \\ S = S_1; S_2 \\ (p, \mathcal{RT}) = \mathcal{RT}_1 \circ_{p_1, p_2} \mathcal{RT}_2 \end{array}}{\vdash SPR_1 \text{ then } SPR_2 ::_c S, \mathcal{RT}, p}$$

Proof Calculus for Refinements

Results



Theorem (Soundness)

Let SPR be a refinement specification such that $\vdash SPR \triangleright \square$ and all generic units in the architectural specifications appearing in SPR are consistent. If $\vdash SPR ::_c S$, then there is \mathcal{R} such that $\vdash SPR \Rightarrow \mathcal{R}$ and $\mathcal{R} \models S$.

$$\frac{\vdash \text{cons}(USP)}{\vdash \text{cons}(USP \text{ qua SPEC-REF})}$$

$$\frac{\vdash \text{cons}(SPR)}{\vdash \text{cons}(USP \text{ refined via } \sigma \text{ to } SPR)}$$

$$\frac{\vdash \text{cons}(SPR) \text{ for all } UN : SPR \text{ in } ASP}{\vdash \text{cons}(ASP)}$$

$$\frac{\vdash \text{cons}(SPR_i)}{\vdash \text{cons}(\{U_i \text{ to } SPR_i\}_{i \in \mathcal{J}})}$$

$$\frac{\begin{array}{l} \vdash \text{cons}(SPR_1) \\ \vdash \text{cons}(SPR_2) \end{array}}{\vdash \text{cons}(SPR_1 \text{ then } SPR_2)}$$

Consistency Calculus Results



Theorem (Soundness)

If $\vdash SPR ::_c \square$, the calculi for checking consistency of structured specifications and conservativity of extensions are sound and $\vdash cons(SPR)$, then SPR has a model.

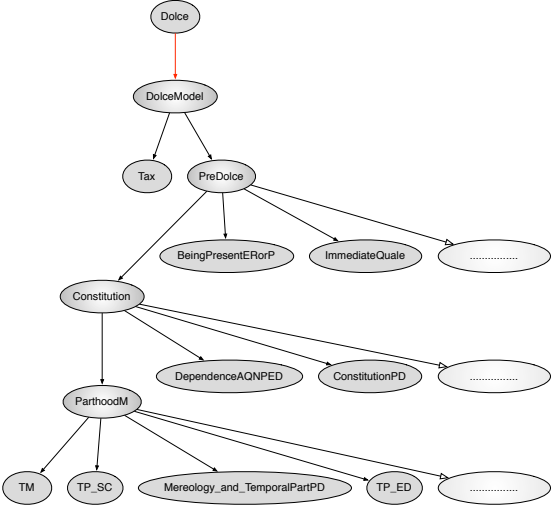
Theorem (Completeness)

If unit imports are omitted, the calculi for checking consistency of structured specifications and conservativity of extensions are complete, $\vdash SPR ::_c \square$ and SPR has a model, then $\vdash cons(SPR)$.

DOLCE: Descriptive Ontology for Linguistic and Cognitive Engineering contains several hundreds of axioms \Rightarrow model finders fail

- ▶ first attempt: architectural spec structure follows that of structured spec \Rightarrow **failed** (due to DEPENDENCE)
- ▶ second attempt **followed structure of taxonomy** \Rightarrow successful
- ▶ by using a strengthening of DEPENDENCE, we could rely on **stronger assumptions** for the interpretation of DEPENDENCE for various subconcepts when extending it to a superconcept.
- ▶ architectural spec has **38 units**
 - ▶ well-formedness check using HETS not feasible
 - ▶ after **split** into four architectural specs, well-formedness check using HETS took 35h on i7
 - ▶ the split leads to a **refinement tree with 4 branchings**
- ▶ DOLCE models can now be built in a **modular way**

Dolce: Refinement tree for model construction



- ▶ logic-independent framework for refinements
- ▶ based on institutions resp. specification frames
- ▶ tool support through **Heterogeneous Tool Set** www.dfki.de/sks/hets
- ▶ **specialized** notion of refinements via **institution comorphisms**
- ▶ open question: **completeness of refinement calculus**